

**JH. TRANSIENT GATE**  
based on SPL and Drawmer circuits

ACTIVE 



INPUT OUTPUT  
unbalanced Stereo TRS

BYPASS

ATTACK

-2 0 +2  
-4 +4  
-6 +6  
-9 +9  
-15dB +15dB

SUSTAIN

-3 0 +3  
-6 +6  
-10 +10  
-15 +15  
-24dB +24dB

GAIN REDUCTION

60 50 40 30 20 10 0dB 100dB  
80 90

THRESHOLD

-34 -36 -40 -46 -50 -52 -54dB  
-20 -10 off

ATTACK TIME

0.1 0.15 0.25 0.5 1 20m 50m 70m  
20us 1s

HOLD TIME

0.1 0.2 0.3 0.5 1 15m 50m 2ms 2s

RELEASE TIME

0.1 0.3 0.5 0.6 1 2 3 3.5 4s 2ms 30m

(c) J. Haible 2005



POWER